# **Expresso DMA Core**

## **Product Highlights**

- Provides high performance, scattergather DMA operation
- Works with Northwest Logic Expresso
  Cores and FPGA PCI Express hard cores
- Can be configured for up to 1024 DMA Channels
- Supports AXI Master and Slave interfaces of selectable data widths 32,64,128 or 256-bit.
- Supports PCIe Multi-Function and SR-IOV capability
- Supports Endpoint and Rootport applications
- Supports legacy, MSI, MSI-X and local AXI interrupts
- Fully hardware validated
- Companion Windows and Linux Expresso DMA Drivers available
- Provided with a PCI Express Testbench
- Delivered fully integrated with target PCI Express core
- Minimal ASIC gate count
- Source code available
- Customization and Integration services available

#### **Block Diagram** WR REQ WR REQ RD REQ RD REQ PCIe RX AXI Master AXI Master Interface RD CPL AXI RD REORDER QUEUE RD CF RD CPI PCIe RD PCIe RD CPL REORDER QUEUE RD REQ AXI RD REQ NTER-CONNEC RD CPL Interface ∎TX PCIe TX WR REC WR REQ Multi-Channel RD REQ . DMA Registers & MSFX Table RD CPL RD REQ DMA SRC DMA RD CPL RD REQ DMA DST RD CPL SGL Cache SGL RD REQ DMA RD CPL Transaction Execution WR REQ PCIe Clock Domain AXI Clock Domain

### **Product Overview**

The Northwest Logic Expresso DMA Core provides highperformance, scatter-gather DMA operation in a flexible fashion. This enables the core to be easily integrated and used in a wide variety of DMA-based systems.

Key features which contribute to this flexibility include:

- Works with Northwest Logic soft Expresso Cores and FPGA hard cores
- Can be configured with multiple DMA Channels which are independently controlled by software
- Supports Addressed transfers
- Supports DMA registers to be accessible from both PCIe and AXI interface
- Supports DMA scatter-gather list and completion Status Queue to be located either in PCIe or AXI memory
- Supports legacy, MSI, MSI-X and local AXI interrupts

Using the core eliminates the need for the user to implement their own DMA design significantly reducing development time and risk.

In addition, Northwest Logic provides companion Windows and Linux Expresso DMA Drivers. The Expresso DMA Driver works hand-in-hand with the Expresso DMA Core to implement scatter -gather DMA operation.

Northwest Logic also provides board support packages for a wide variety of Northwest Logic and third party PCI Express boards. This support packages including a working FPGA design, Driver and GUI binaries. Contact Northwest Logic for more information.

Northwest Logic also provides IP Core customization services. Contact Northwest Logic for a quote.

#### **Product Deliverables:**

- Core (Netlist or Source Code)
- Testbench (Source Code) with support for Aldec
- Complete Documentation
- Expert Technical Support & Maintenance Updates