The DSI Controller Core is part of Northwest Logic’s MIPI Solution. This solution is designed to achieve maximum MIPI throughput while being easy to use.

The core implements all three layers defined by the DSI Specification: Pixel to Byte Packing, Low Level Protocol, and Lane Management and is fully compliant with the DSI specification. Separate Host (Tx) and Peripheral (Rx) versions of the core are provided.

The core provides 4 data and 1 control/status packet interfaces. The data interfaces can be optional adapter to DBI-B/C and DPI interfaces. The control/status interface can be optionally adapted to an AXI interface. The core supports command and video modes, 1 to 4 data lanes at 1.5+ Gbit/s and all data types.

The core uses the byte lane clock minimizing power consumption and ensuring the core can be used in older process technologies.

The core is delivered fully integrated and verified with the user’s target MIPI PHY. Contact Northwest Logic for a complete list of supported PHYs.

Northwest Logic also provides a DSI Demonstration System which includes an FPGA Board, MIPI Interface Card and MIPI Display. Contact Northwest Logic for more information.

Northwest Logic also provides a complete set of quick-turn design services including IP Customization and Logic, Board, Software Development services. Contact Northwest Logic for a quote.

Product Deliverables:
- Core (Netlist or Source Code)
- Testbench (Source Code) with support for Aldec
- Complete Documentation
- Expert Technical Support & Maintenance Updates