

FPGA Design & Verification in Aldec Active-HDL

Lab 1 – Getting Started

Starting Active-HDL & Creating a Design

1. Start Active-HDL by either:
 - Double-click on the Active-HDL icon , or
 - Selecting All Programs → Aldec → Active-HDL 9.2

Note:

If you select Always open last workspace option before clicking OK, you will not see the Getting Started dialogue during startup again; Active-HDL will open the last workspace instead.

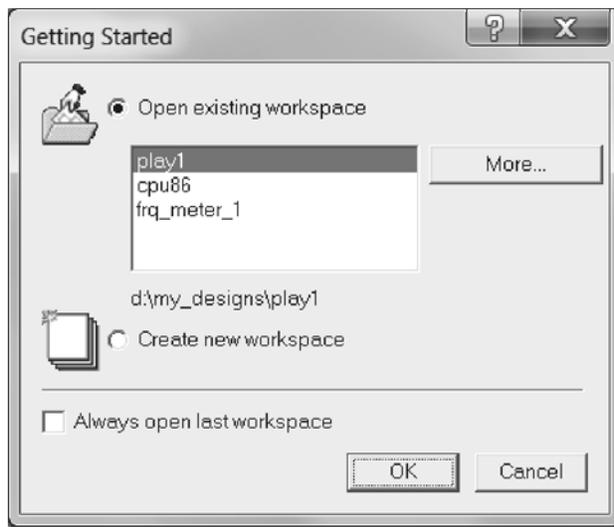


Figure 1: Getting Started Window

Wait until the splash screen with the Active-HDL name and version number appears, once Active-HDL starts the first dialogue you will usually see is the Getting Started dialogue (See Figure 1)

2. From the Getting Started dialogue you can:
 - Open an existing workspace
 - Create new workspace

Select the **Create new workspace** option and click **OK**.

Note

If Active-HDL is already open you can create a new Workspace or Design from the File menu.